

# BHOJ REDDY ENGINEERING COLLEGE FOR WOMEN

Department Of Information Technology

Time Table for for the Academic Year 2015-16

II B.Tech I Semester IT Section-B

Room No:410 South Block(SB)

w.e.f: 01-07-2015

	0930-1030	1030-1130	1130-1230	1230-1330	1330-1430	1430-1530	1530-1630
	1	2	3		4	5	6
<b>Mon</b>	<b>EDC</b>	<b>MFCS</b>	Tutorial BEE-G1(410) MFCS-G2(402) P&S-G3(408)	<b>L U N C H</b>	<b>P&amp;S</b>	<b>DS</b>	<b>DLDCO</b>
<b>Tue</b>	←-----DS LAB-----→				<b>MFCS</b>	Tutorial MFCS-G1(410) BEE-G2(402) DS-G3(408)	<b>EDC</b>
<b>Wed</b>	<b>DLDCO</b>	Tutorial DS-G1(410) EDC-G2(402) DLDCO-G3(408)	<b>EDC</b>		<b>BEE</b>	<b>P&amp;S</b>	<b>DS</b>
<b>Thu</b>	<b>P&amp;S</b>	<b>BEE</b>	<b>DS</b>		Tutorial EDC-G1(410) P&S-G2(402) MFCS-G3(408)	<b>DLDCO</b>	<b>MFCS</b>
<b>Fri</b>	<b>BEE</b>	Tutorial DLDCO-G1(410) DS-G2(402) BEE-G3(408)	<b>DLDCO</b>		←-----EE LAB-----→		
<b>Sat</b>	<b>MFCS</b>	<b>DS</b>	<b>P&amp;S</b>		Tutorial P&S-G1(410) DLDCO-G2(402) EDC-G3(408)	<b>EDC</b>	<b>BEE</b>

S.No	Subject	Name of the Faculty	Dept
1	Mathematical Foundations of Computer Science	E.Nitya	IT
2	Digital Logical Design & Computer Organisation	M.Srividya	IT
3	Data Structures	M.Vinod	IT
4	Basic Electrical Enggineering	Pramod Singh	EEE
5	Electronic devices & Circuits	Asma Begum	ECE
6	Probability & Statistics	K.Padma	MATHS
7	Data structures Lab	M.Vinod, M.Srividya, D.Navaneetha	IT
8	Electrical and Electronics Lab	R.Manju Bhargavi, S.K.val/Asma Begum, RVNR Suneel krishna	EEE/ECE

Tutorial Groups(According to Serial Nos.)

G1: 1-20(410)

G2: 21-40(402)

G3: 41-57(408)